SHEET 1 OF 1

| INFORMATION DISCLOSURE CITATION IN AN APPLICATION | | | | ATTY. DOCKET NO. 49657-700 | | SERIAL NO. | | | |
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| | | | | APPLICANT Tsutomu YOSHIMURA, et al. | | | | | |
| (PTO-1449) | | | | FILING DATE June 01, 2000 | | GROUP | | | |
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| I | OTHER | ART (Inch | ding Author | Title, Date, P | ertinent P | ages, Etc.) | | 1 | ┨. |
| KCT | "A 30-MHz Hy IEEE JOURNA 1385-1394 | brid Analog | g/Digital Cloc | k Recovery Cir | rcuit in 2-μ | m CMOS", I | B. Kin ER 19 | n et al., 990, pp. | |
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| EXAMINER Khomloon gtran DATE CONSIDERED 09/15/2005 | | | | | | | | | |

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.